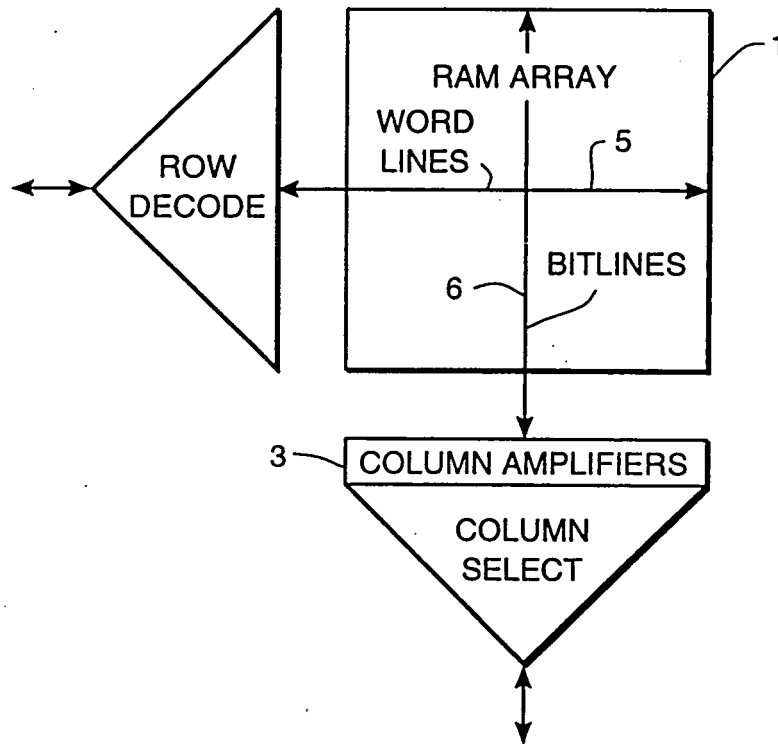


000140" 84351560

FIG 1



17

FIG 2

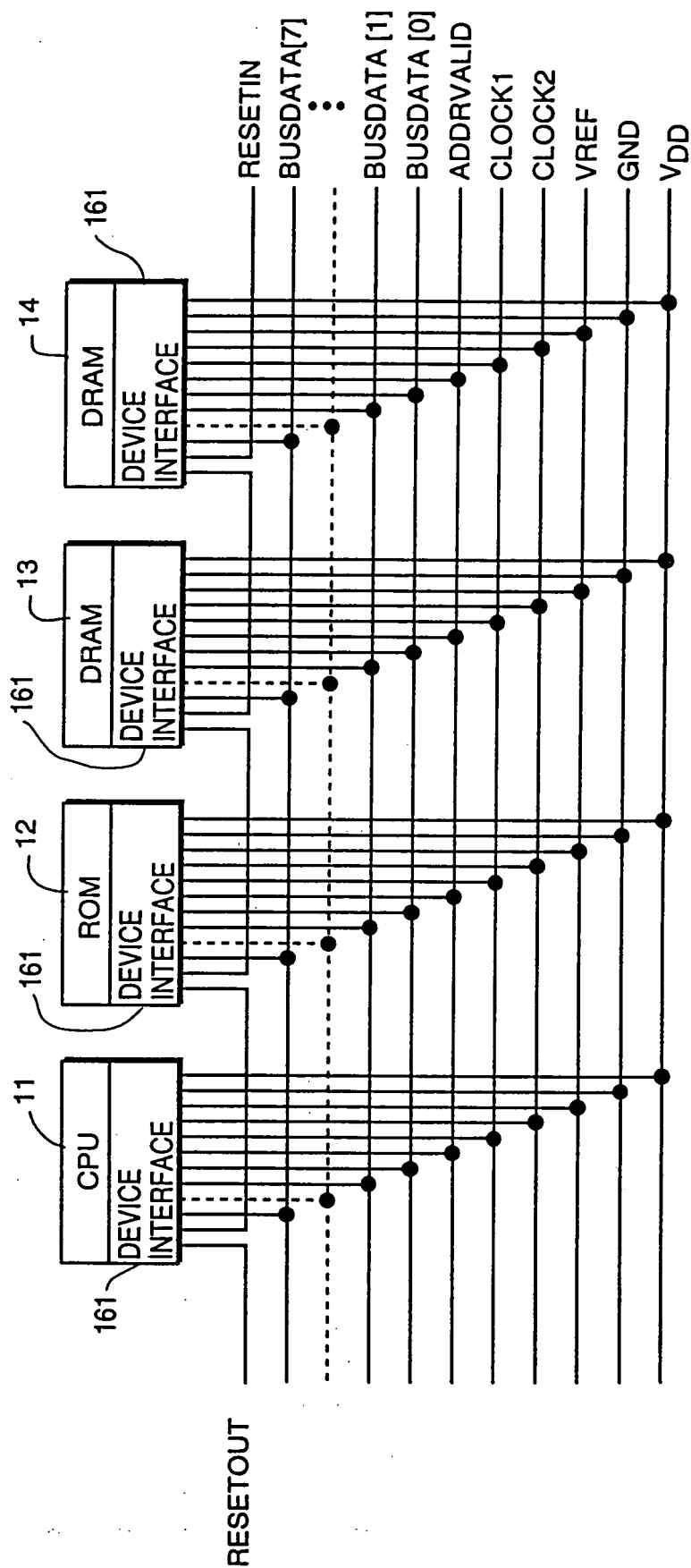
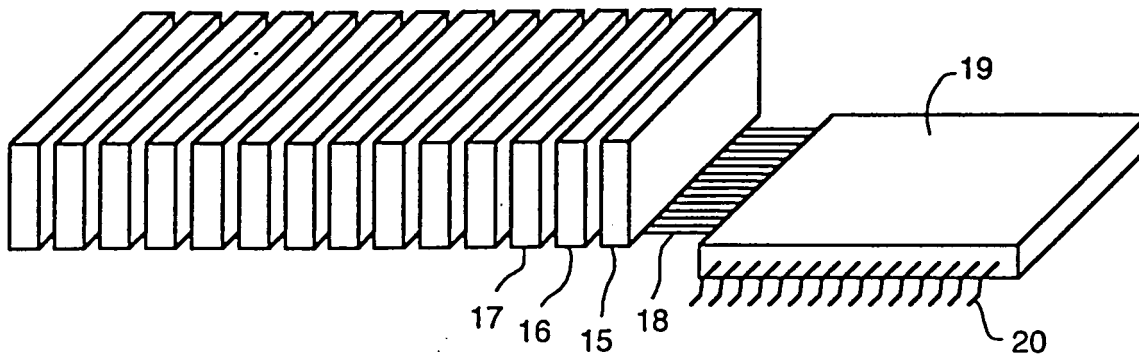


FIG 3



REGULAR ACCESS

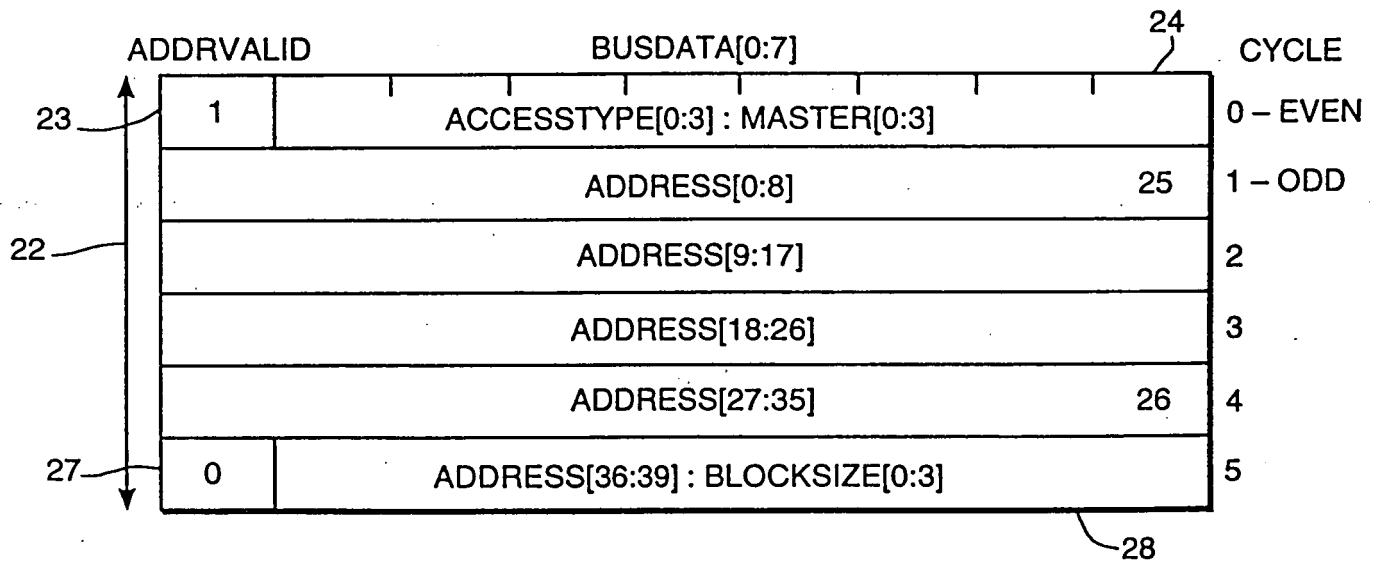


FIG 4

REJECT (NACK) CONTROL PACKET

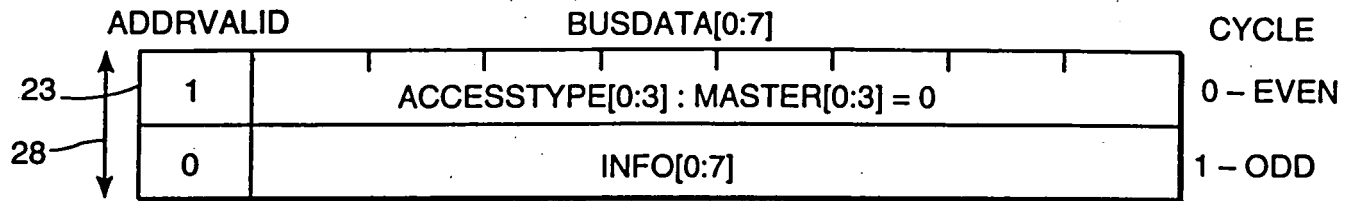


FIG 5

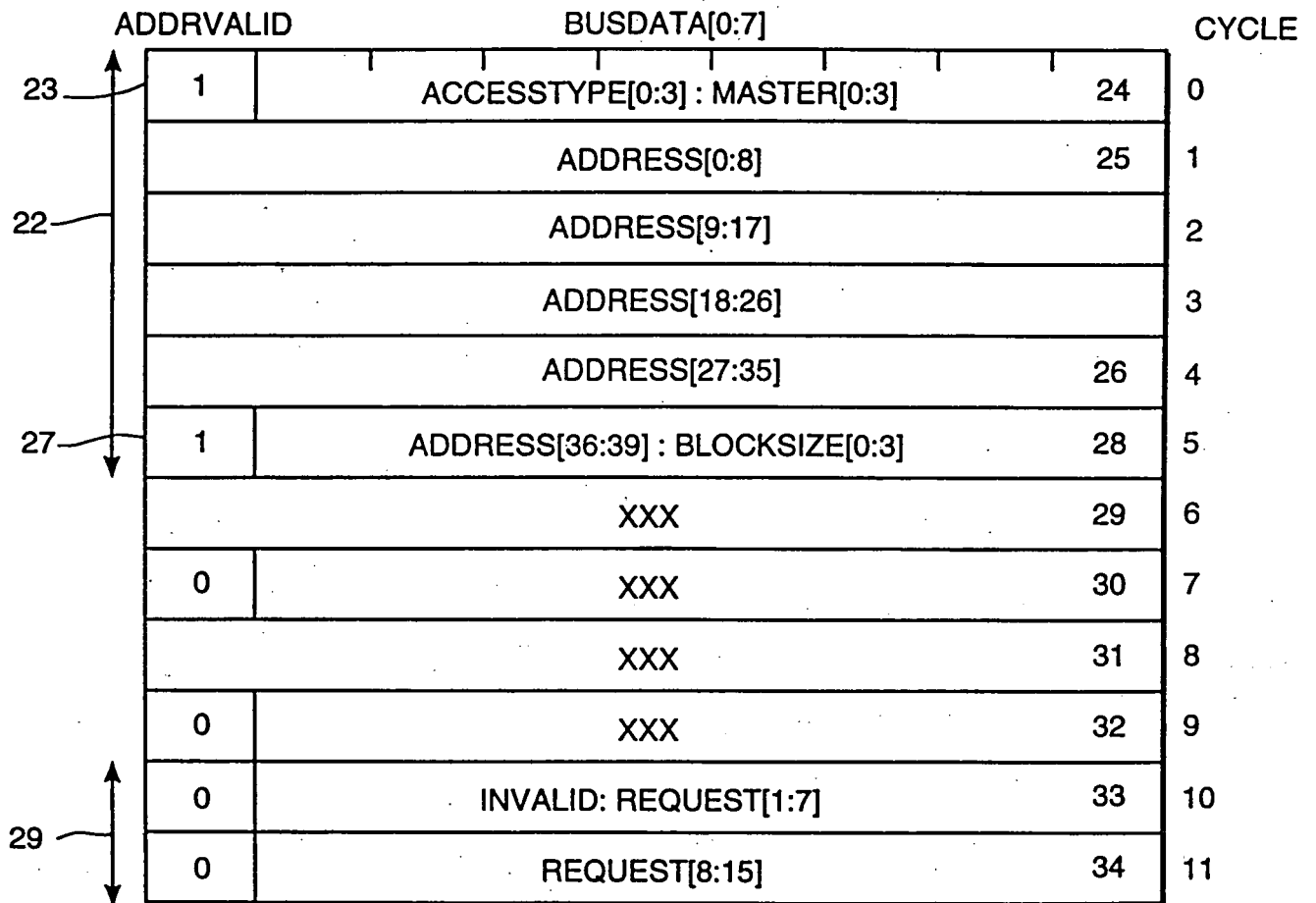
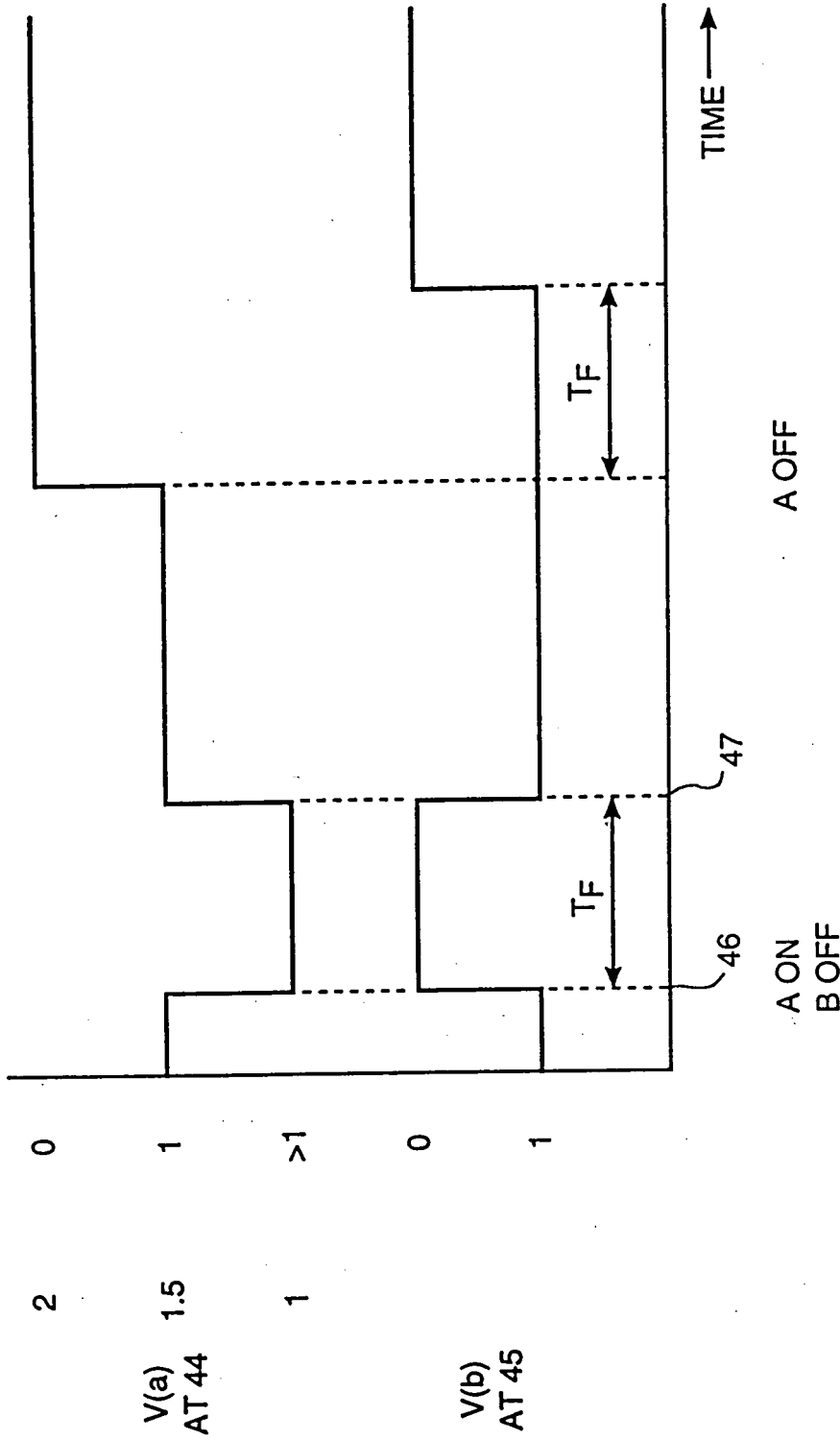
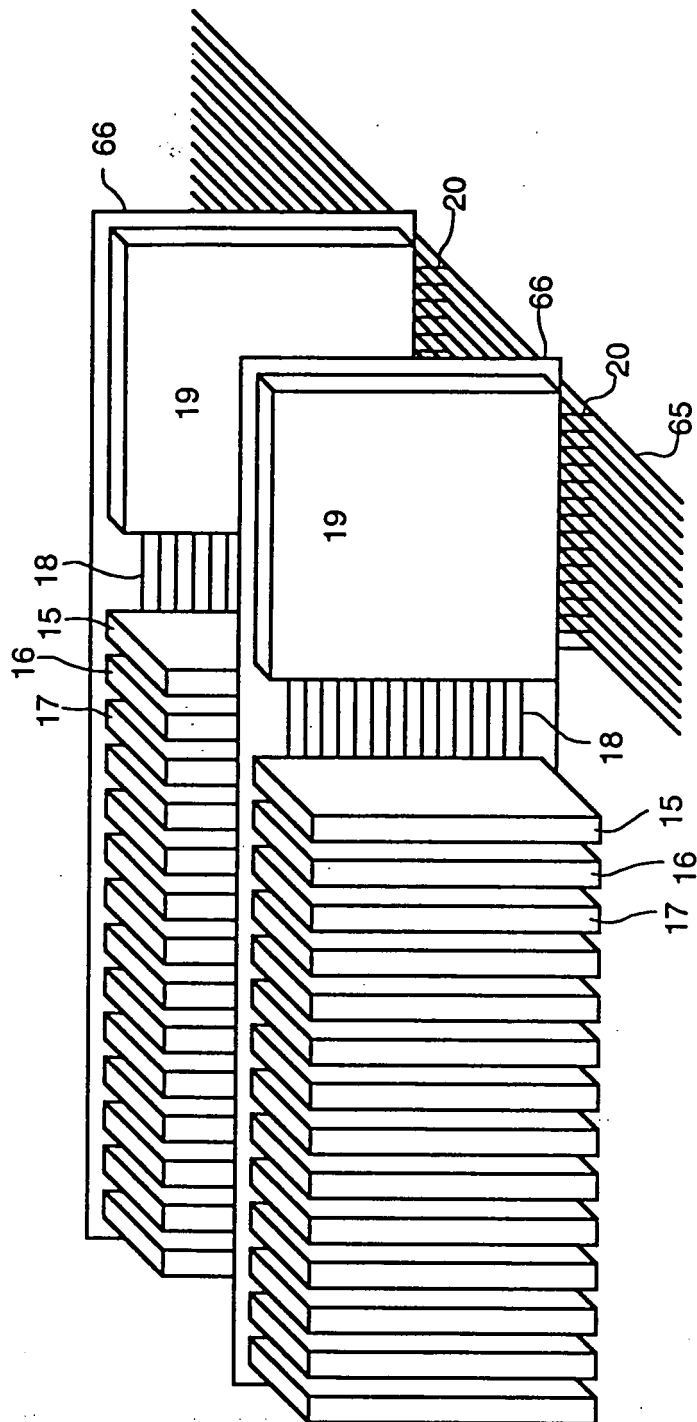


FIG 6

FIG_7A

VOLTAGE LOGICAL
VALUE





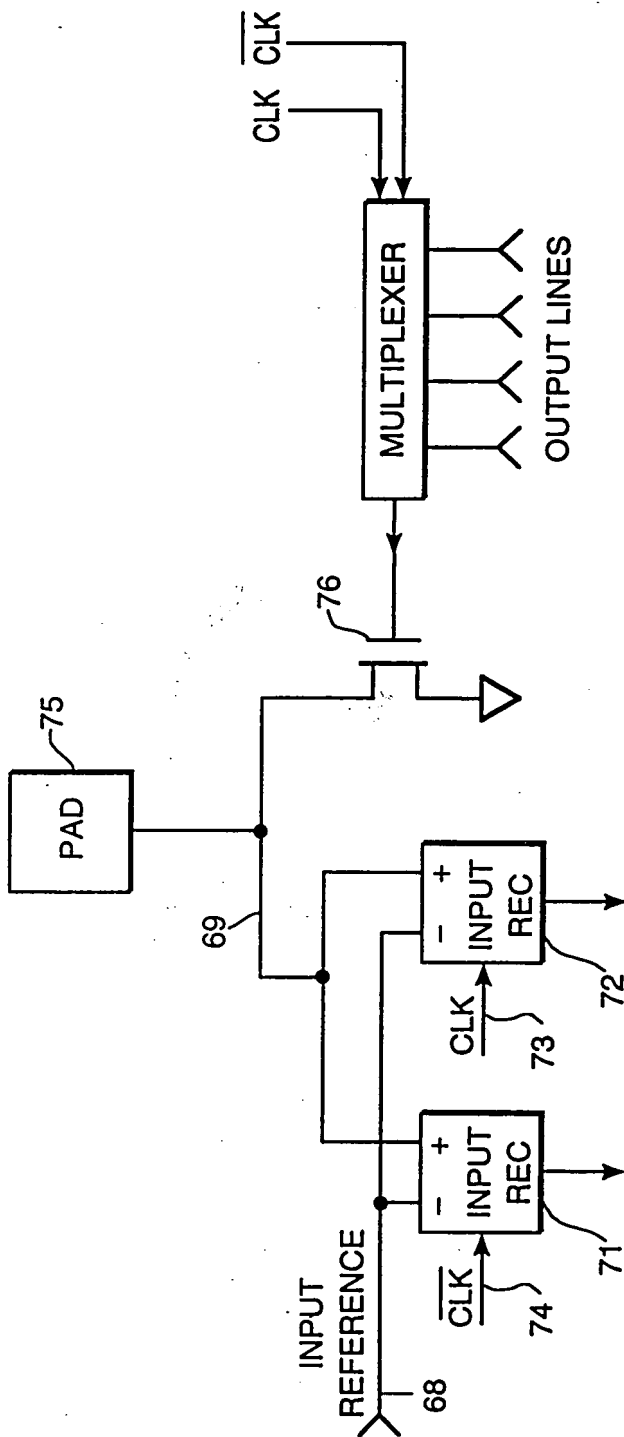


FIG 10

FIG 11

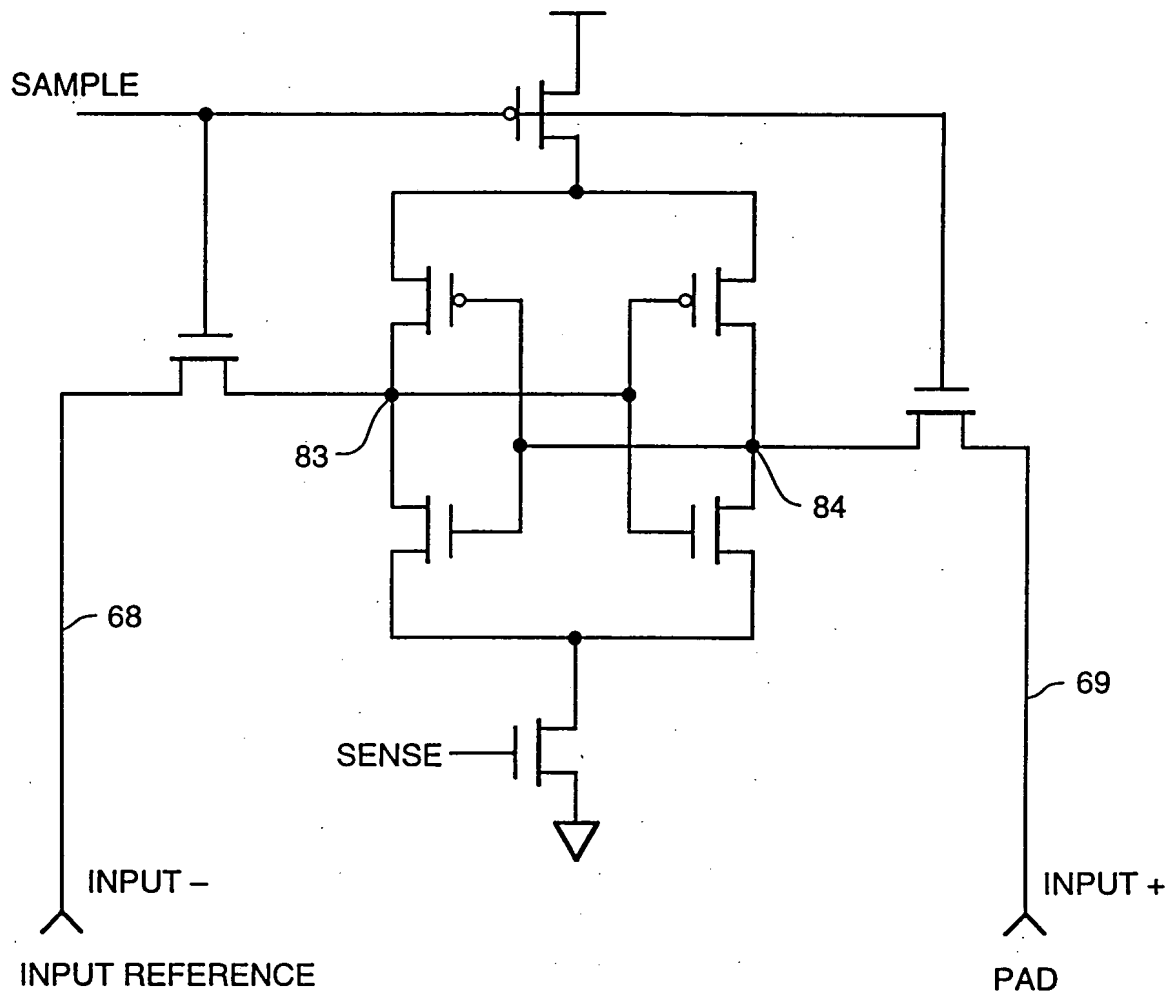


FIG 12

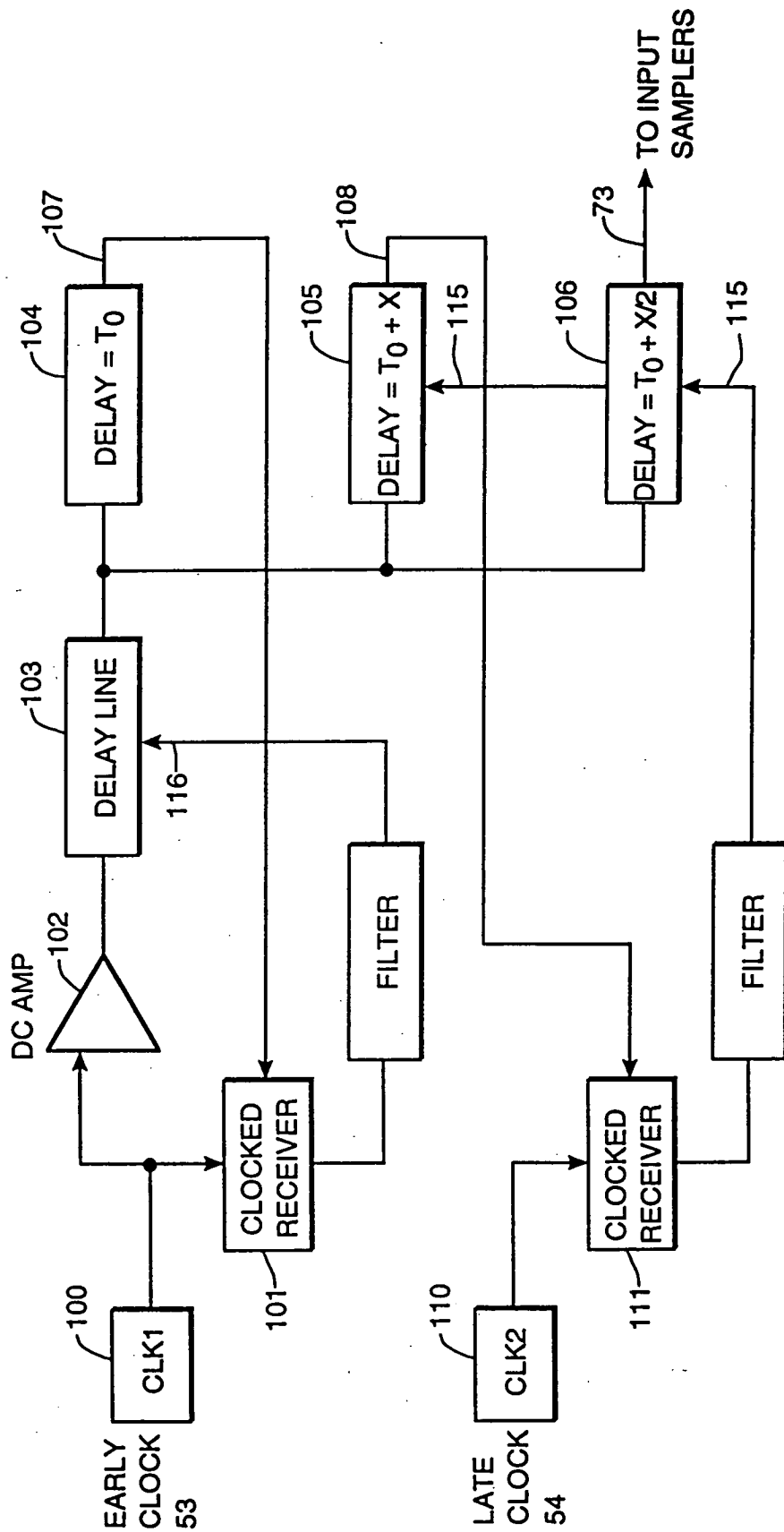
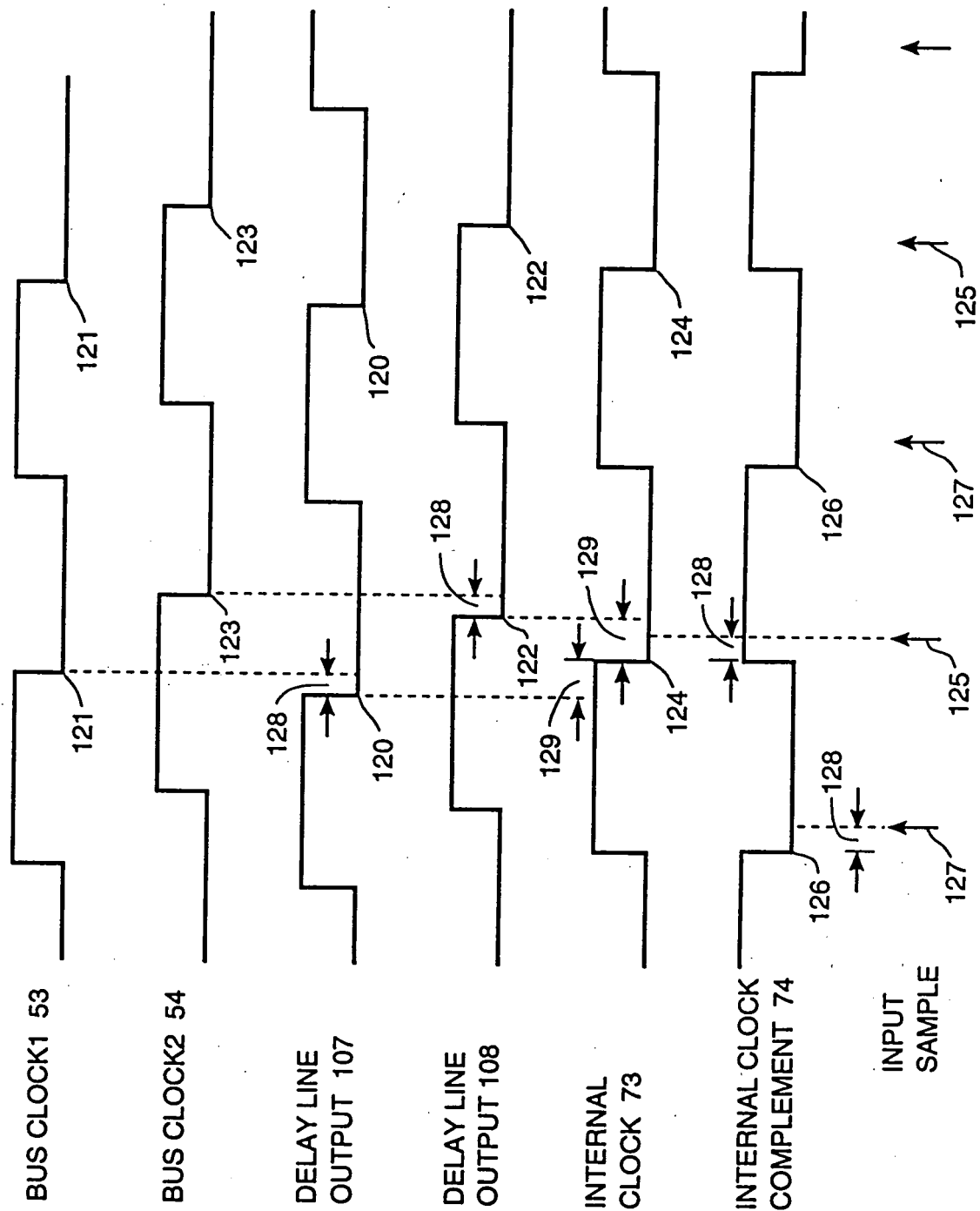


FIG. 13



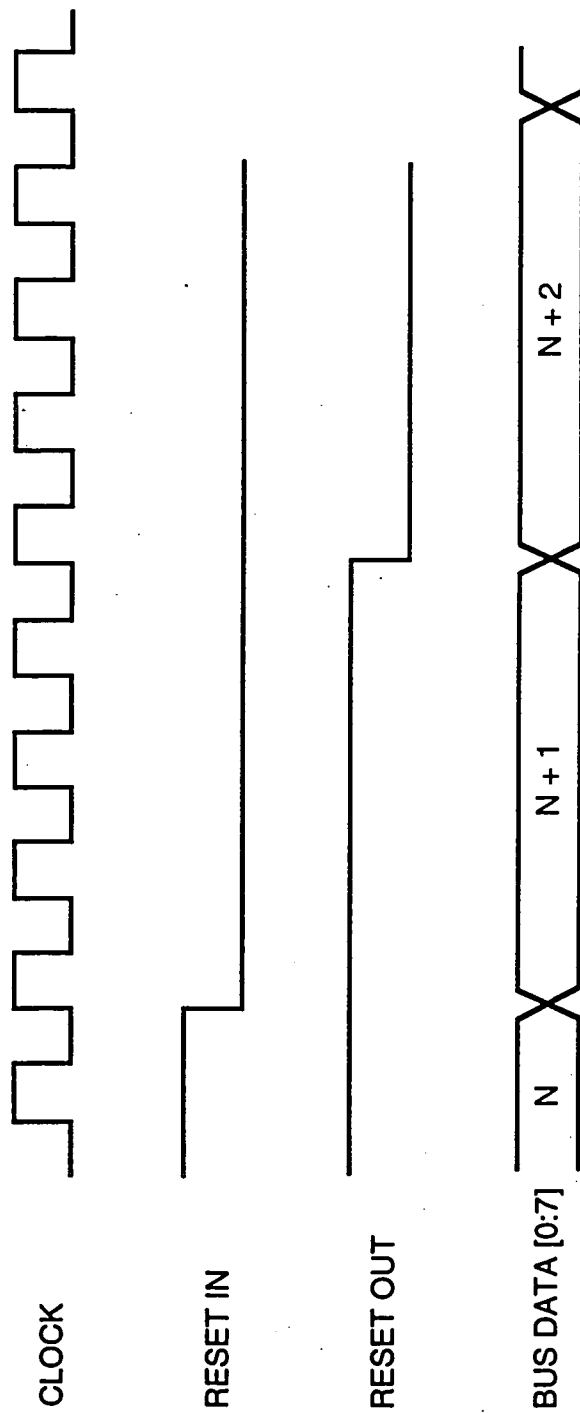


FIG 14

